

268



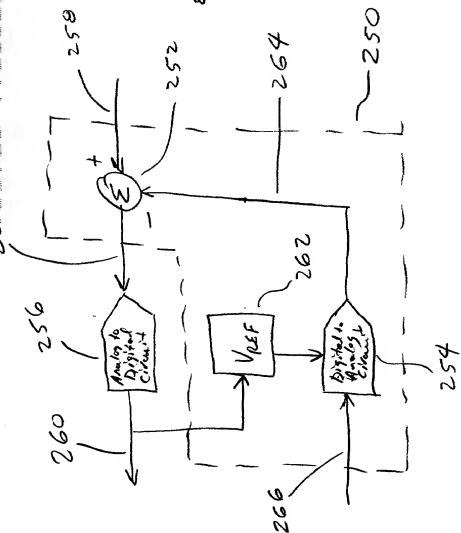


FIG. 3

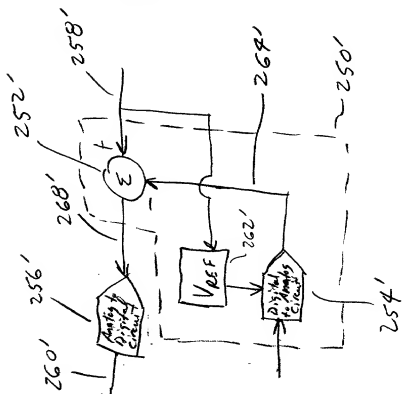


FIG. 4

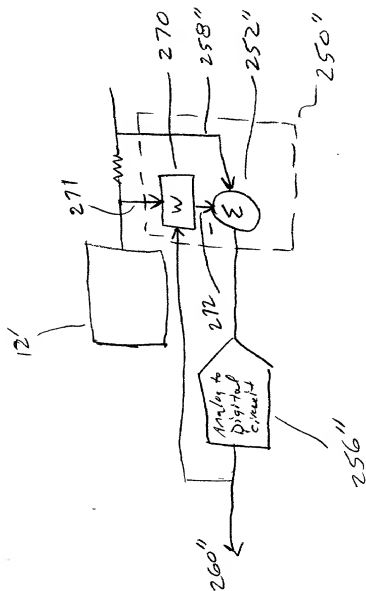
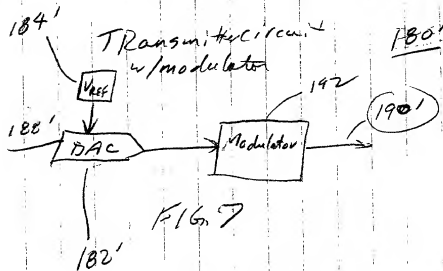
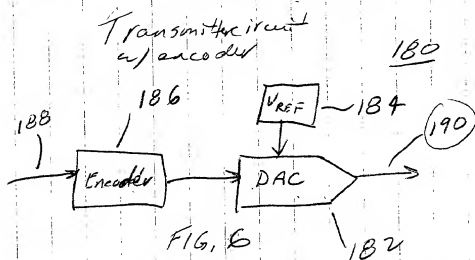


FIG. 5

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FIG. 6



09700241.021601

688

Receive w/decoder

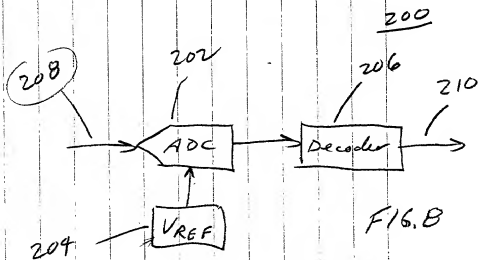


FIG. 8

Receive w/ demodulator

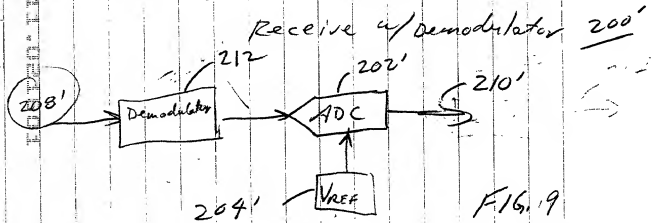


FIG. 9

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ADSL Barrier Communications

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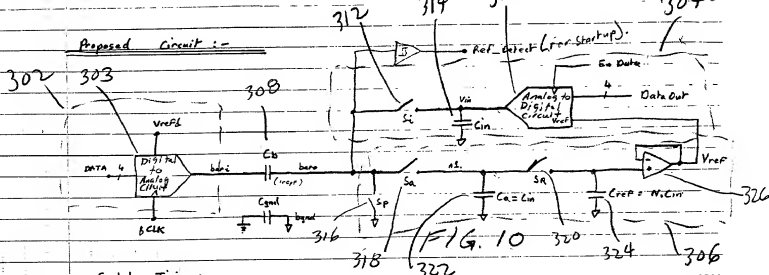


Fig. 11

~~Cref is chosen to be $N \times C_a$. This means that V_{ref} is a moving average of the previous N Ref pulses. This allows some immunity to 'dud' Ref pulses. Without averaging, a 'dud' Ref pulse could wipe out successive data cycles. For a 4-bit converter, N should be greater than or equal to 16. This allows only Ref errors to be less than one LSB size. The exact value of N is not terribly important.~~

WITNESSED AND UNDERSTOOD

SIGNED
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